Boot mode	0=P1_1	1=P1_2	2=P2_8	3=P2_9	Description
USART0	ON	ON	ON	ON	Boot from device connected to USART0 using pins P2_0 and P2_1. For flash parts, enter UART ISP mode.
SPIFI	OFF	ON	ON	ON	Boot from Quad SPI flash connected to the SPIFI interface on P3_3 to P3_8. (Default boot on Jawbreaker)
EMC 8-bit	ON	OFF	ON	ON	Boot from external static memory (such as NOR flash) using CS0 and an 8-bit data bus.
EMC 16-bit	OFF	OFF	ON	ON	Boot from external static memory (such as NOR flash) using CS0 and a 16-bit data bus.
EMC 32-bit	ON	ON	OFF	ON	Boot from external static memory (such as NOR flash) using CS0 and a 32-bit data bus.
USB0	OFF	ON	OFF	ON	Boot from USB0.
USB1	ON	OFF	OFF	ON	Boot from USB1.
SPI (SSP)	OFF	OFF	OFF	ON	Boot from SPI flash connected to the SSP0 interface on P3_3 (function SSP0_SCK), P3_6 (function SSP0_MISO), P3_7 (function SSP0_MOSI), and P3_8 (function SSP0_SSEL).
USART3	ON	ON	ON	OFF	Boot from device connected to USART3 using pins P2_3 and P2_4. For flash parts, enter UART ISP mode.

HackRF Jawbreaker Boot mode ON=VCC and OFF=GND
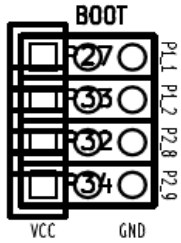
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